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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/004,888	12/07/2001	Kazuyuki Sato	04329.2704	7604	
22852	7590 08/24/2005		EXAMINER		
	N, HENDERSON, FAF	ELAMIN, ABDELMONIEM I			
LLP 901 NEW Y	ORK AVENUE, NW	ART UNIT	PAPER NUMBER		
	TON, DC 20001-4413	2116			
			DATE MAILED: 08/24/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application	n No	Applicant(s)				
Office Action Summary		Application						
		10/004,88		SATO, KAZUYUKI				
	omoo Aodon Gammary	Examiner		Art Unit				
	The MAILING DATE of this communication	A Elamin	solver shoot with the o	2116	drocs			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply								
THE - External efter - If the - If NO - Failu Any (	ORTENED STATUTORY PERIOD FOR REMAILING DATE OF THIS COMMUNICATIOnsions of time may be available under the provisions of 37 CFR SIX (6) MONTHS from the mailing date of this communication.  I period for reply specified above is less than thirty (30) days, a period for reply is specified above, the maximum statutory per the toreply within the set or extended period for reply will, by started the period by the Office later than three months after the maded patent term adjustment. See 37 CFR 1.704(b).	N. 1.136(a). In no ever reply within the state iod will apply and wi atute, cause the app	ent, however, may a reply be tim utory minimum of thirty (30) days Il expire SIX (6) MONTHS from ication to become ABANDONEI	nely filed s will be considered timely the mailing date of this co D (35 U.S.C. § 133).				
Status								
1) 又	Responsive to communication(s) filed on 03	7 December 2	001.					
· —	This action is <b>FINAL</b> . 2b) This action is non-final.							
3)								
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.							
Disposition of Claims								
5)⊠ 6)⊠ 7)□	4)  Claim(s) 1-18 is/are pending in the application.  4a) Of the above claim(s) is/are withdrawn from consideration.  5)  Claim(s) 1-8 and 15-18 is/are allowed.  6)  Claim(s) 9-14 is/are rejected.  7)  Claim(s) is/are objected to.  8)  Claim(s) are subject to restriction and/or election requirement.							
Applicati	ion Papers							
9) The specification is objected to by the Examiner.								
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.								
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.								
·	,	Examiner. No	ne the attached Office	Action of form P1	O-152.			
•	under 35 U.S.C. § 119							
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>								
Attachmen	t(s)							
1) Notice of References Cited (PTO-892)  4) Interview Summary (PTO-413)								
	e of Draftsperson's Patent Drawing Review (PTO-948)	00)	Paper No(s)/Mail Da 5) Notice of Informal P		)-152)			
	mation Disclosure Statement(s) (PTO-1449 or PTO/SB/ r No(s)/Mail Date <u>7/14/04; 12/7/01</u> .	6) Other:	atont Application (FTC	r-192j				

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### **DETAILED ACTION**

## Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 9-14 rejected under 35 U.S.C. 102(b) as being anticipated by Saito, US. Pat. No. 5,867,579.
- 3. Claim 9, Saito teaches a hardware management apparatus [title, abstract] comprising: a first area storing intellectual property information [3<sup>rd</sup> Area 37 of Fig. 4];
- a second area storing management information used to manage the intellectual property information upon manufacture [ROM 18 of Fig. 4]; and
- a third area storing management information used to manage the intellectual property information after change [2<sup>nd</sup> Area 36 of Fig. 4], wherein the intellectual property information stored in the first area is read out under a condition of the management information stored in the second and third areas [abstract, Fig. 4 and related disclosure].
- 4. Claim 10, Saito teaches each of the management information stored in the second and third areas includes an access number condition for the intellectual property information [col. 11, line 65 thru col. 12, line 28].
- 5. Claims 11 and 13, Saito teaches a semiconductor integrated circuit comprising [title, abstract]:
  - a plurality of semiconductor components [Figs. 3-4]; and

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a memory storing management information used to manage mount state of the plurality of semiconductor components [EEPROM 31 of fig. 4], wherein the management information is rewritten, when the mount state of the plurality of semiconductor components changes, to indicate changed mount state of the plurality of semiconductor components [abstract, col. 10, line 66 thru col. 11, line 8].

6. Claims 12 and 14, Saito teaches the memory includes a first information area storing information of the plurality of semiconductor components upon manufacture and a second information area storing information of the plurality of semiconductor components after change [col. 12, lines 16-18].

## Allowable Subject Matter

7. Claims 1-8 and 15-18 are allowed.

### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to A Elamin whose telephone number is (571) 272-3674. The examiner can normally be reached on MON-FRI 9:30 AM - 6:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne Browne can be reached on (571) 272-3670. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Plamin

Primary Examiner

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August 18, 2005